

## **Tektronix Announces TekExpress DDR5 Transmitter Solution** **Developed to let engineers quickly, efficiently and reliably validate and debug DDR5 designs**

BEAVERTON, Ore., May 27, 2020 -- [Tektronix, Inc.](#) today launched its new TekExpress DDR5 Transmitter Solution which allows engineers to overcome DFE analysis challenges with improved automation, confidently and efficiently validate and debug DDR5 designs with user-defined acquisition and master DDR5 de-embedding techniques with Serial Data Link Analysis (SDLA).

5G is driving rapid growth in a variety of exciting technologies - from augmented reality and artificial intelligence to cloud computing and IoT - making DDR5 more important than ever. Rapid access to high volumes of stored data means complex designs are pushing boundaries of signal integrity, requiring higher performance measurements for compliance, debugging and validation.

TekExpress DDR5 Transmitter Solution is an automated system-level test application that lets users quickly, efficiently and reliably validate and debug DDR5 designs to meet more than 50 electrical and timing measurements as defined in JEDEC.

### **DFE Analysis**

The best tools for debugging DDR3/4 are insufficient when testing DDR5 designs in the presence of inter-symbol interference (ISI). Tektronix' DDR5 system level compliance software provides a variety of automated tools to overcome the challenges that come with the next generation of DDR including:

- Rx DFE Equalization support for the write data eye measurement on DDR5 traffic
- Automated measurement for 50+ DDR5 electrical and timing parameters as defined in JEDEC
- New algorithms to consistently and reliably differentiate between read and write bursts
- New compliance application architecture with enhanced automation to reduce test time and help bring designs to market faster.

### **Debugging and Validation**

The TekExpress DDR5 Transmitter Solution puts control back in the hands of the engineer with a user defined acquisition mode which runs DDR5 JEDEC compliance measurements by customizing scope settings including sample rate, record length, bandwidth and more.

Tektronix' standalone DDR5 DFE application provides total control over the DFE Gain and 4 tap values allowing engineers to run internal test plans, perform simulation of measurement correlation to fine tune simulation models and conduct what-if analysis by changing 4-tap and gain values.

### **SDLA**

Validating s-parameters is often the primary concern when de-embedding DDR5 designs. With improved passivity checking, port assignments and plotting capabilities, Serial Data Link Analysis (SDLA) enhance s-parameter file validation, improves flexibility and increases confidence in the de-embedding process while saving time. Other debug software tools require users to complete the entire process to find results. The TekExpress DDR5 Transmitter Solution allows users to detect problems earlier and allows for debug and optimization of designs more efficiently.

### **Availability**

The Tektronix TekExpress DDR5 Transmitter Solution is available now worldwide. For more information go to <https://www.tek.com/memory-technologies>.

### **About Tektronix**

Tektronix, Inc., headquartered in Beaverton, Oregon, delivers innovative, precise and easy-to-operate test, measurement and monitoring solutions that solve problems, unlock insights and drive discovery globally. Tektronix has been at the forefront of the digital age for over 70 years. More information on our products and solutions is available at [Tek.com](#).

Follow us on [Twitter](#), [Facebook](#), [Instagram](#) and [LinkedIn](#) to stay connected. Learn more from our engineers on the Tektronix [blog](#) and read our latest announcements in our [Newsroom](#).

Tektronix is a registered trademark of Tektronix, Inc. All other trade names referenced are the service marks, trademarks or registered trademarks of their respective companies.

---

<https://news.tektronix.com/DDR5>